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XA-9625A
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.: 10/816,925

Group Art Unit: 2878

Filed: April 5, 2004

Examiner: S. Allen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGINING
SYSTEM

Allowed: October 5, 2004

Confirmation No.: 3100

REQUEST FOR ACKNOWLEDGEMENT OF RECEIPT
OF CERTIFIED COPY OF PRIORITY DOCUMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Attn: Mail Stop Issue Fee

Sir:

Referring to Item 4 on the Notice of Allowability mailed October 5, 2004, as stated in the footnote on page 2 of the Utility Patent Application Transmittal for this application filed April 5, 2004, a certified copy of the Japanese patent application underlying the claim of priority under 35 U.S.C. § 119 was filed in prior Application No. 10/080,586 filed February 25, 2002, of which the present application is a continuation.

Applicants respectfully request acknowledgement of the receipt of the certified copy.

Respectfully submitted,

NHS:lat

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October 29, 2004